Docket No. 5000-5296

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Ta

Takefumi NISHIMUTA, et al.

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2826

Filed:

December 13, 2005

Examiner:

Tuan N. QUACH

For:

DC AMPLIFIER AND SEMICONDUCTOR INTEGRATED CIRCUIT

THEREFOR

AMENDMENT UNDER 37 C.F.R. § 1.111

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This paper is responsive to the non-final Office Action dated October 22, 2007. This paper is set forth as follows:

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 5 of this paper.